

ABSTRACT OF DISCLOSURE

A plasma display panel in which partition walls are formed to have different widths includes a front substrate and a rear substrate facing the front substrate. The front substrate includes sustain electrodes, a dielectric layer that covers the sustain electrodes, and a protective layer formed on the bottom of the dielectric layer. The rear substrate includes address electrodes, partition walls formed parallel to the address electrodes discharge spaces therebetween, the partition walls having corresponding different widths. Red, green and blue phosphor layers are deposited on the corresponding insides of adjacent pairs of the partition walls.

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